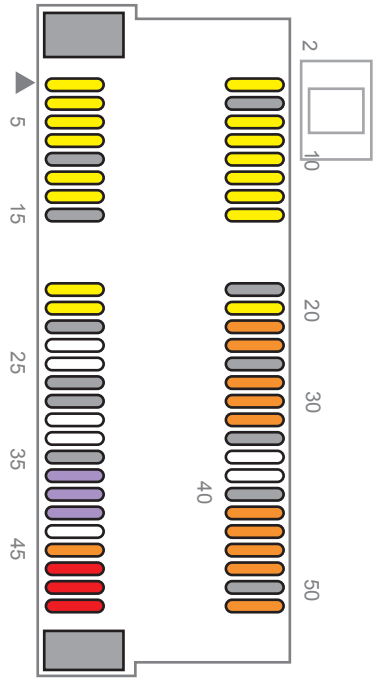
EeePC connector

FLASH_CON



*gray is GND.

EeePC FLASH_CON

1	D0	2	D15
3	D1	4	GND
5	D2	6	D14
7	D3	8	D13
9	GND	10	D12
11	D4	12	D11
13	D5	14	D10
15	GND	16	D9
17	D6	18	GND
19	D7	20	D8
21	GND	22	RESET
23	PERn0 ???	24	IOWR
25	PERp0 ???	26	GND
27	GND	28	IORD
29	GND	30	REG(DMAACK)
31	PETn0 ???	32	INPACK(DMARQ)
33	PETp0 ???	34	GND
35	GND	36	USB_D-
37	A00	38	USB_D+
39	A01	40	GND
41	A02	42	IORDY
43	N.C.	44	INTRQ
45	PDIAG	46	CS0
47	3.3V	48	CS1
49	3.3V	50	GND
51	3.3V	52	DASP

Note :
- Please check wiring well and do not neglect a check with a tester.
- Make the length of wiring as the same as possible.
- Recommend that wiring of a power supply and GND uses thick wiring.

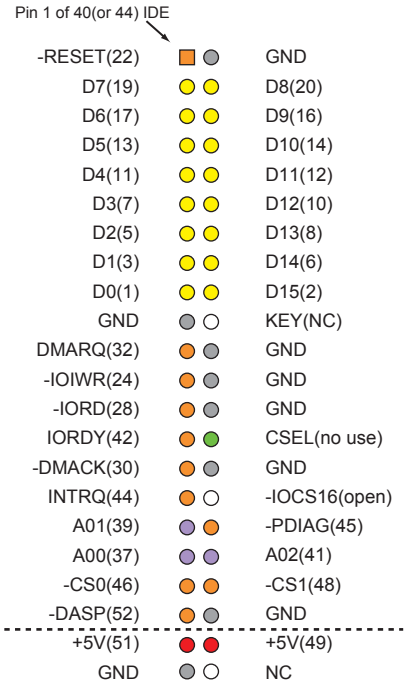
A problem peculiar to an adapter
- A conversion adapter should use the thing corresponding to DMA. In the case of a PIO mode, patterns may differ.
- When recognition does not go well, it may be necessary to remove the LED relation of a DASP signal.

Let' s enjoy EeePC Hacking!!

EeePC Hack with in Compact Flash

A plan, data creation : mao
A check of operation : mao & tie2
PDF creation : tie2
first appearance: 2008.03.09
translation&fix: 2008.03.15

CF-IDE Adapter



( )The number is pins of FLASH_CON.

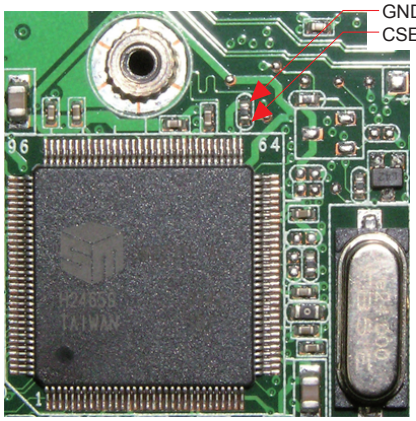
IDE(3.5 or 2.5) to FLASH_CON

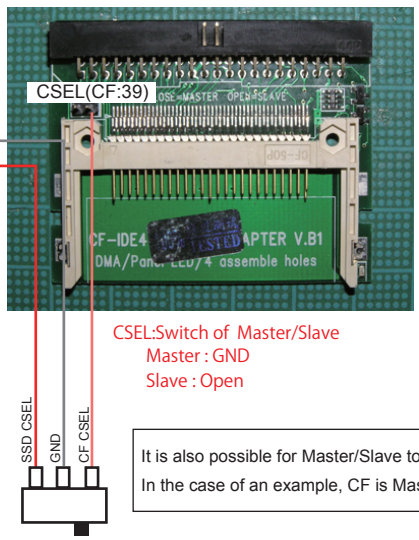
Pin	Name	Type	FLASH_CON	Comment	Pin	Name	Type	FLASH_CON	Comment
1	-RESET	I	22		2	GND	—		**2
3	D7	I/O	19		4	D8	I/O	20	
5	D6	I/O	17		6	D9	I/O	16	
7	D5	I/O	13		8	D10	I/O	14	
9	D4	I/O	11		10	D11	I/O	12	
11	D3	I/O	7		12	D12	I/O	10	
13	D2	I/O	5		14	D13	I/O	8	
15	D1	I/O	3		16	D14	I/O	6	
17	D0	I/O	1		18	D15	I/O	2	
19	GND	—		**2	20	KEY(NC)	—	—	
21	DMARQ	—	32	CF to 43pin	22	GND	—		**2
23	-IOWR	I	24		24	GND	—		**2
25	-IORD	I	28		26	GND	—		**2
27	IORDY	O	42		28	CSEL	I	—	**1
29	-DMACK	—	30	CF to 44pin	30	GND	—		**2
31	INTRQ	O	44		32	-IOCS16	O	—	
33	A01	I	39		34	-PDIAG	I/O	45	
35	A00	I	37		36	A02	I	41	
37	-CS0	I	46		38	-CS1	I	48	
39	-DASP	I/O	52		40	GND	—		**2
The following is only 2.5									
41	+5V	—	51	CF to 3.3V	42	+5V	—	49	CF to 3.3V
43	GND	—		**2	44	NC	—	—	

**1 : no use.Master/Slave is switched on PCB.
**2 : If GND is connected by CF-IDE PCB, wiring is good at two. Because, it is since +5V is two.

EeePC(SSD) modification

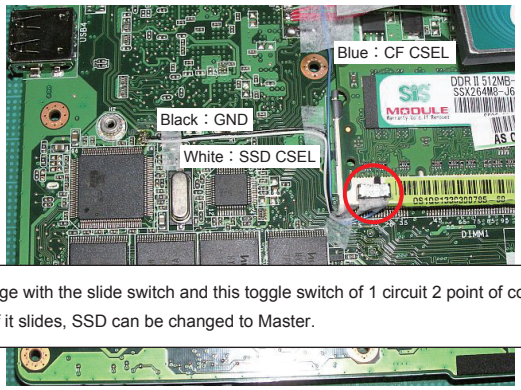
The chip resistance is removed,
and the cable is connected.





CSEL:Switch of Master/Slave
Master : GND
Slave : Open

example:installing switch



It is also possible for Master/Slave to change with the slide switch and this toggle switch of 1 circuit 2 point of contact.
In the case of an example, CF is Master. If it slides, SSD can be changed to Master.